



Table 1- CPU CORE EVALUATION BOARD



1.5 Million Gate based General Purpose Actel FPGA based Kit designed for developing hands on expertise on the FPGA based design and development.

Following interfaces are existing on the hardware:-

- **256k (32k x 8) Parallel Paged EEPROM for external storage of important data such as neutron flux profiles etc.- AT28C256-15PU**
- **256-kbit (32 k x 8) SRAM for soft IP core data storage - CY62256VNLL-70SNXI**
- **OTP EPROM for program storage / important parameters / security keys - AT27LV010A-70JU**
- **+5V Powered Multichannel RS-232 Driver/Receiver Interface for PC Communication- MAX232ACSE**
- **OCTAL BUFFERS/DRIVERS WITH 3-STATE OUTPUTS - 74LVC541AD**

THIS HAS BEEN DEVELOPED FOR IGCAR, KALPAKKAM FOR IN HOUSE EXPERTISE DEVELOPMENT.